



Job Description

947 Senior Logic/Synthesis Engineer

Job Number: TTI_A_2014_23
Job Location: Austin, Texas
Report To: Director of Test Chip Design

Responsibilities:

- Responsible for logic design and synthesis of high frequency embedded processors on the latest TSMC processes.
- Duties will include RTL coding, Functional and gate-level simulation, synthesis, and timing convergence.
- Will need to interface with architects and IP providers for performance tuning.
- Will also interface with integration engineers to enable them to successfully place and route the design.
- These responsibilities will be for TSMC Testchip designs.
- Be able and willing to participate in occasional off-hours conference calls (usually evening calls) and possible overseas travel.

Requirements:

- Must have Bachelors/Masters Computer/Electrical Engineering degree and 7+ years of experience.
- Must have experience in RTL/synthesis experience on high frequency design.

TSMC Technology is an Equal Opportunity Employer.